

### Inventor: Goergen et al. HIGH-SPEED ROUTER BACKPLANE Attorney Docket No. 3981-7/Application No. Not yet assigned

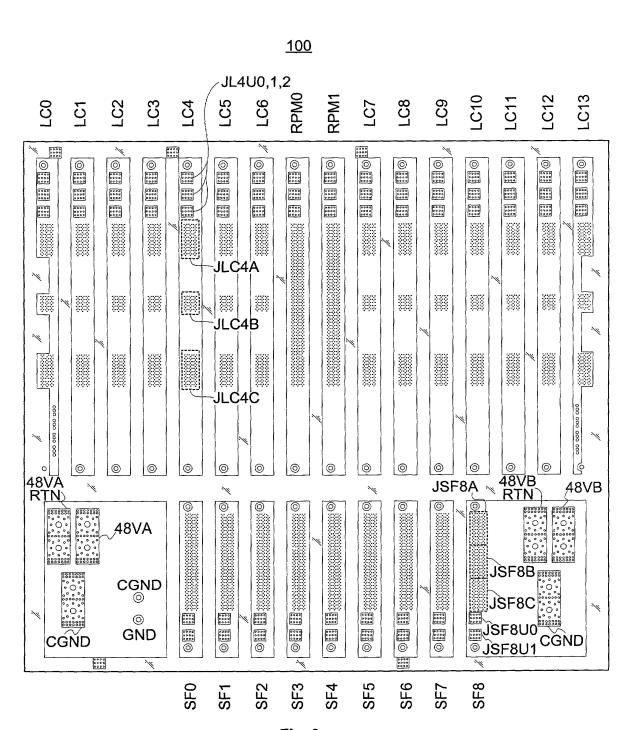


Fig. 3

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<u>100</u>

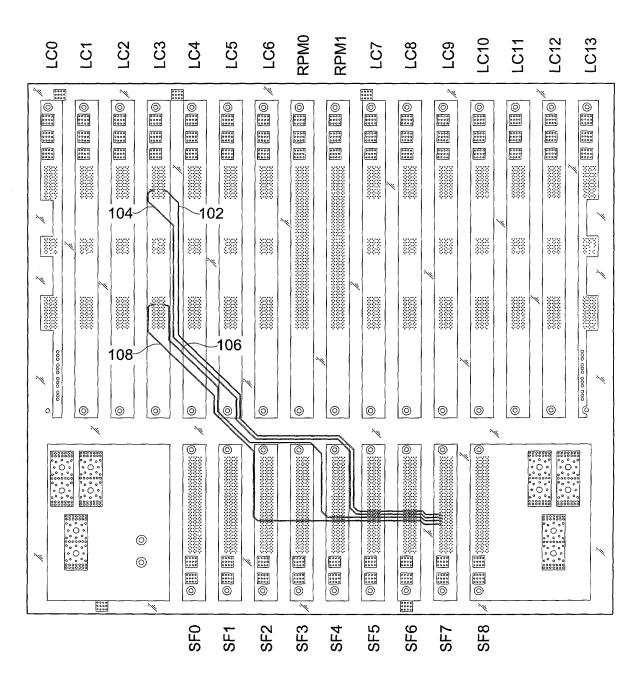


Fig. 4

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<u>110</u> -112a 112f -112b ∴112c -112d \_112e 114a 114b 114c 116a 116b 120a 120b 118a -118b 122a 122c . 122b -124a <sub>-</sub>126c -126a 124b 126b

Fig. 5

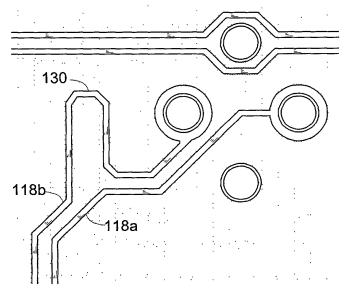


Fig. 6

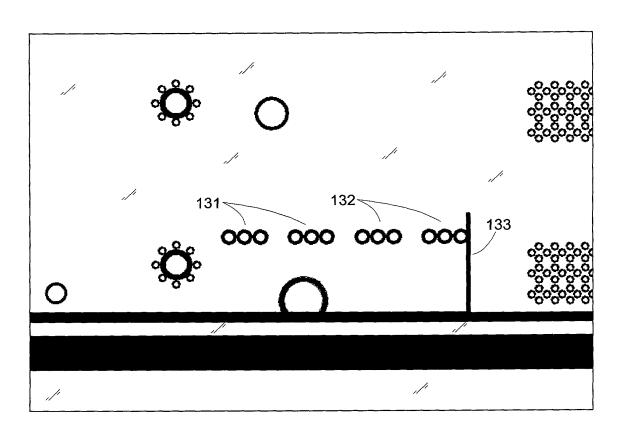


Fig. 7A

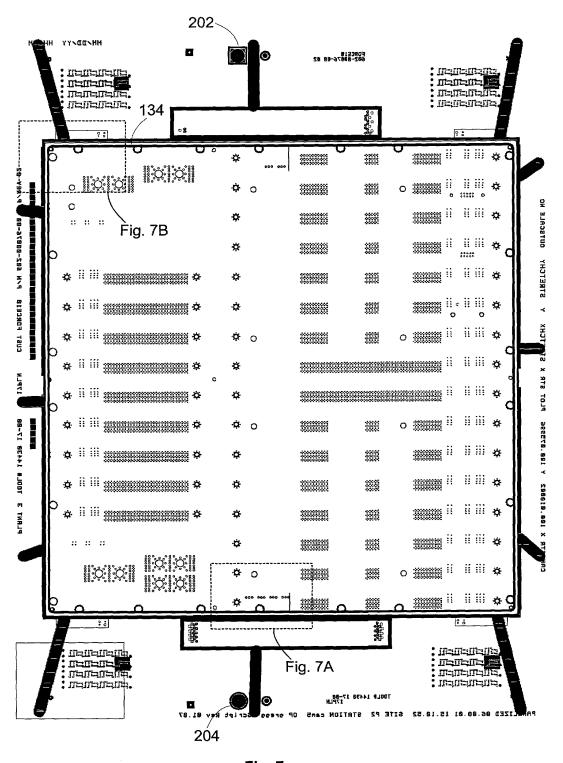


Fig. 7

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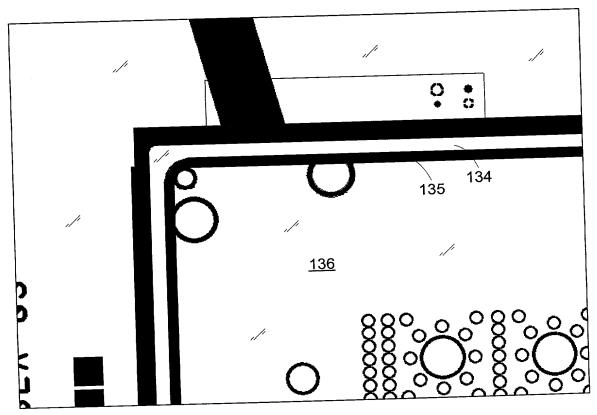
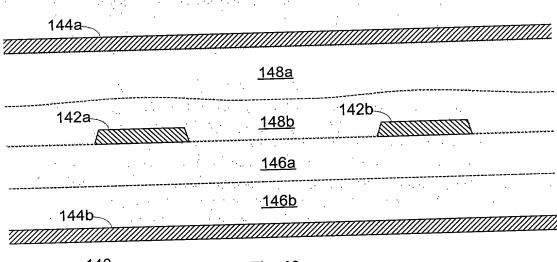


Fig. 7B



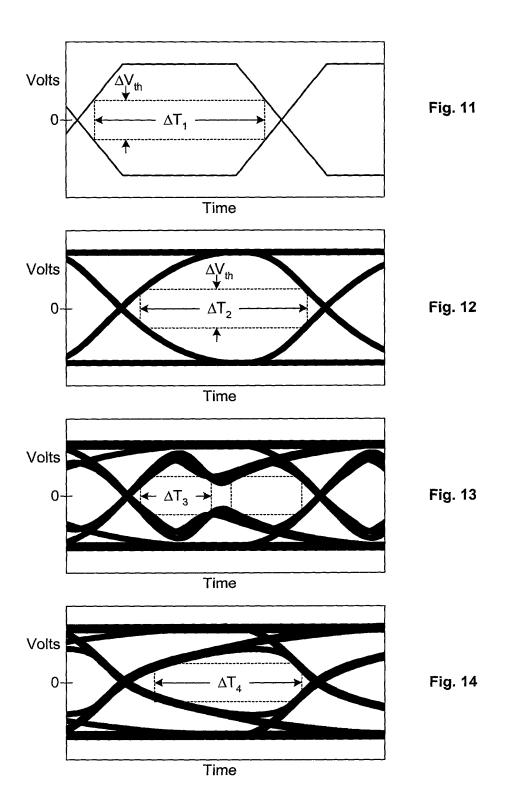
<u>140</u>

Fig. 10

Co	nductive	Layers	8/18	Insulating Layers	
Layer No.	Thickne	ss Type		Type	Thickness
L01{	2.0	Foil/Plating		—Mask	0.7
LO1 \( \)	1.2	Pads—		—⊪B-stage	5.4
L02	1.4	GND-	- <i> </i>	Core	7.5
L03	1.4	HS1		B-stage	6.9
L04	1.4	GND-	- <i> </i>	—в-stage —Core	7.5
L05	1.4	HS2-	- 2000		6.9
L06	1.4	GND-		—B-stage —Core	7.5
L07	1.4	HS3-	- <i> </i>		
L08	1.4	GND-	– <i>- 70000000000000000000000000000</i> –	B-stage	6.9
L09	1.4	HS4	– <i>аптинатинатина</i> –	—Core	7.5
L10	1.4	GND-		-B-stage	6.9
L11	1.4	HS5	 	Core	7.5
L12	1.4	GND-		—B-stage	6.9
L12	1.4	Signal x1—		LS Core	5.0
L14	1.4	Signal y1	- <u>aminuminimum</u> -	LS B-stage	
L15	1.4	GND-		-LS Core	5.0
				LS B-stage	e 6.0
L16	5.6	A 48V rtn	- <u>(</u>	-LS Core	6.0
L17	5.6	A 48V dc			0.0
				LS B-stage	e 8.8
L18	5.6	B 48V dc	- ''''''''''''''''''''''''''''''''''''	-LS Core	6.0
L19	5.6	B 48V rtn-			
L20	1.4	GND		LS B-stage	
L21	1.4	Signal y2—		-LS Core	5.0
L22	1.4	Signal x2—		LS B-stag	
L23	1.4	GND-	<del>-</del>	-LS Core	5.0
L24	1.4	HS6—	- <i>anamamamama</i> -	—B-stage	6.9
L25	1.4	GND	- <i></i> -	Core	7.5
L26	1.4	HS7—	- <i> </i>	—B-stage	6.9
L27	1.4	GND-	- <i>ammunumumu</i>	Core	7.5
L28	1.4	HS8_	<u>-</u> 	B-stage	6.9
L29	1.4	GND-		Core	7.5
L30	1.4	HS9—		B-stage	6.9
L31	1.4	GND-		Core	7.5
L32	1.4	HS10_		—B-stage	6.9
L33	1.4	GND_		Core	7.5
				—B-stage	5.4
L34{	1.2 2.0	Pads— Foil/Plating	7 <u>111111111111111111111111111111111111</u>	Mask	0.7
			F1 A		

Fig. 8

Conductive Layers			9/18	Insulating Layers	
Layer No. T				Type T	hickness
L01{	2.0 1.2	Foil/Plating Pads		Mask	0.7
L02	1.4	GND-		B-stage	5.4 7.5
L03	1.4	HS1-	- <del></del>	Core	7.5
L04	1.4	GND	- <del></del>	-B-stage	6.5
L05	1.4	HS2-	- <del> </del>	-Core	7.5
L06	1.4	GND-		–B-stage	6.5
L07	1.4	HS3	- <i> </i>	Core	7.5
L08	1.4	GND-		–B-stage	6.5
L09	1.4	HS4	- <i></i>	Core	7.5
L10	1.4	GND-	- 	-B-stage	6.5
L11	1.4	HS5	- <del> </del>	Core	7.5
L12	1.4	GND		−B-stage	6.5
L13	1.4	Signal x1 —		-Core	7.5
L14	1.4	Signal y1 —		-B-stage	4.0 7.5
L15	1.4	GND-	- <i>ammanammanama</i> .	Core	
				-Lam 2 B-stg	
L16	5.6	A 48V rtn		-Lam 1 B-stg	8.5
				-Core	6.0
L17	5.6	A 48V dc		–Lam 1 B-stg	1 8.5
Fig. 9				Lam 2 B-stg	
				Lam 1 B-stg	
L18	5.6	B 48V dc		_	
L19	5.6	B 48V rtn		Core	6.0
2.0	0.0	2 .07		-Lam 1 B-stg	8.5
1.00	4.4	OND	<u> </u>	—Lam 2 B-stg	9.0
L20	1.4	GND Signal y2		-Core	7.5
L21 L22	1.4 1.4	Signal x2—	- <del> </del>	–B-stage	4.0
L23	1.4	OND		Core	7.5
L24	1.4	1100	- <i>ammanammanama</i>	–B-stage	6.9
L25	1.4	GND		-Core	7.5
L26	1.4	HS7—	- <i> </i>	–B-stage	6.9
L27	1.4	GND		-Core	7.5
L28	1.4	1100		−B-stage	6.9
L29	1.4	GND-		Core	7.5
L30	1.4	HS9		−B-stage	6.9
L31	1.4	GND	-	-Core	7.5
L32	1.4	HS10		–B-stage	6.9
L33	1.4	GND		-Core	7.5
	1.2	Pads—		-B-stage	5.4
L34{	2.0	Foil/Plating		Mask	0.7



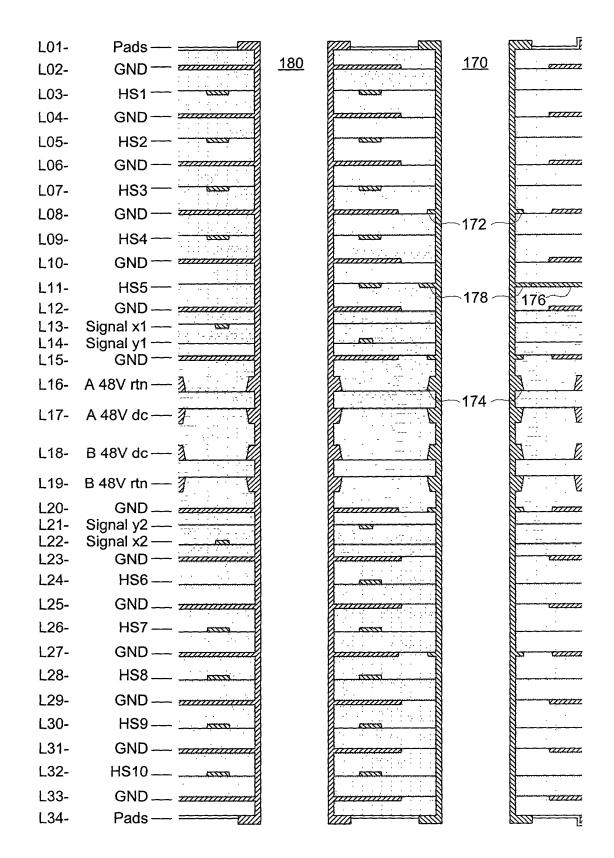
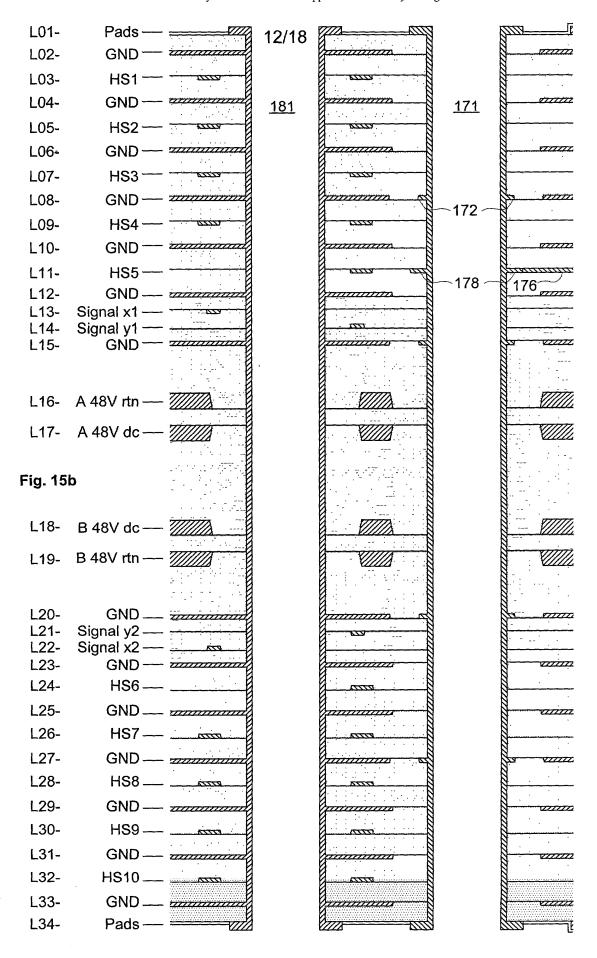


Fig. 15a



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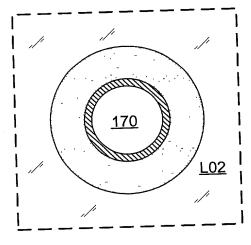


Fig. 16

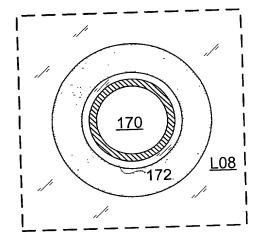


Fig. 17

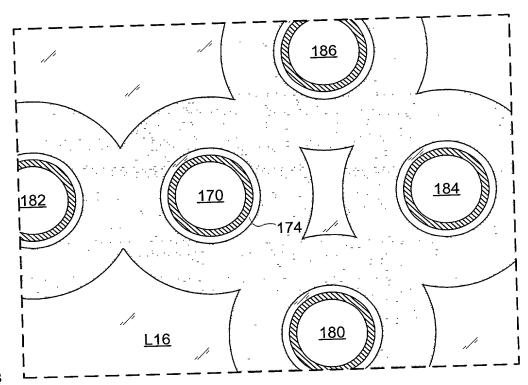
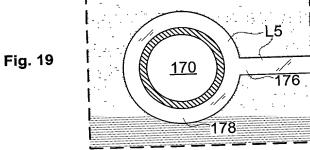
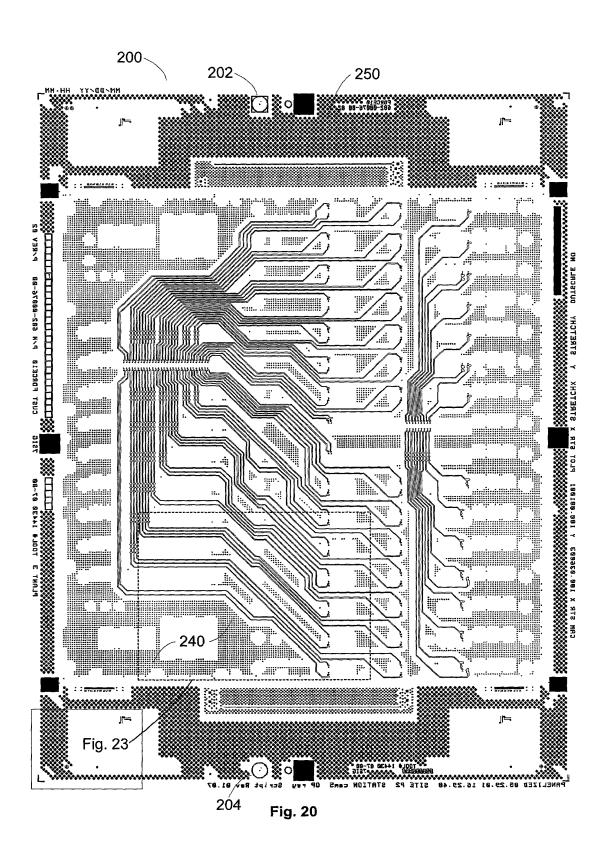
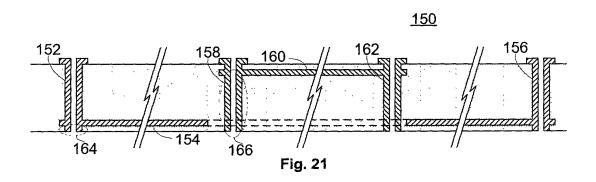


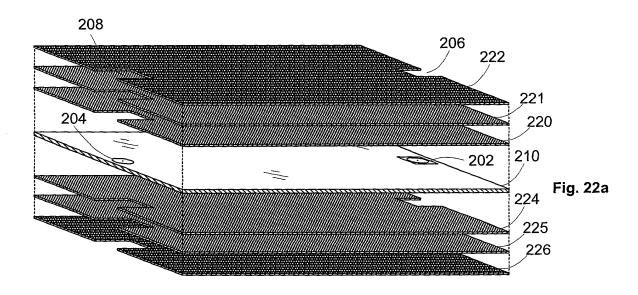
Fig. 18

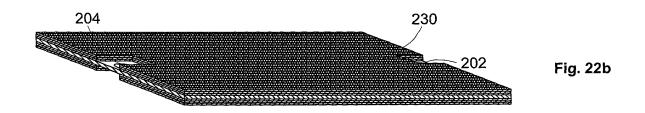




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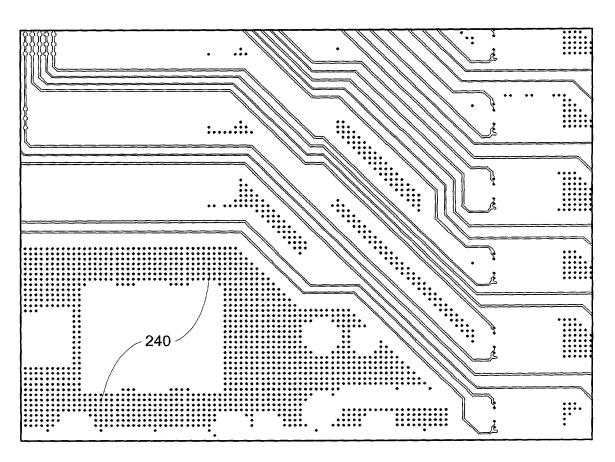


Fig. 23

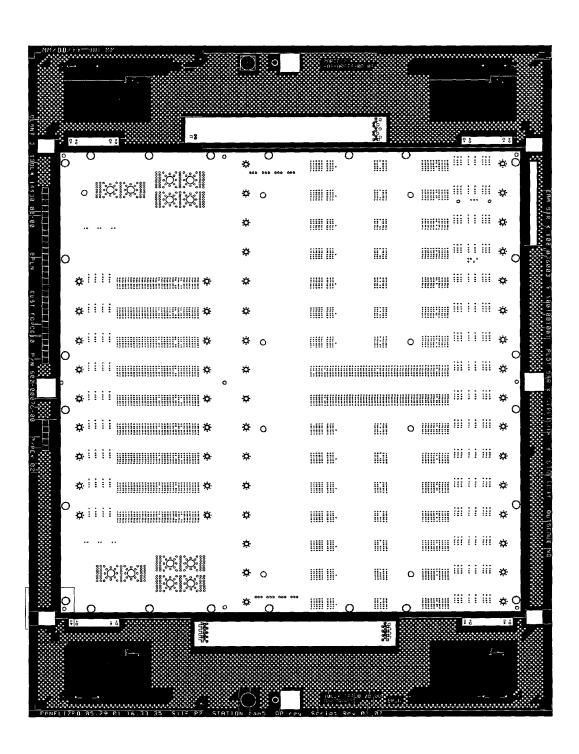


Fig. 24

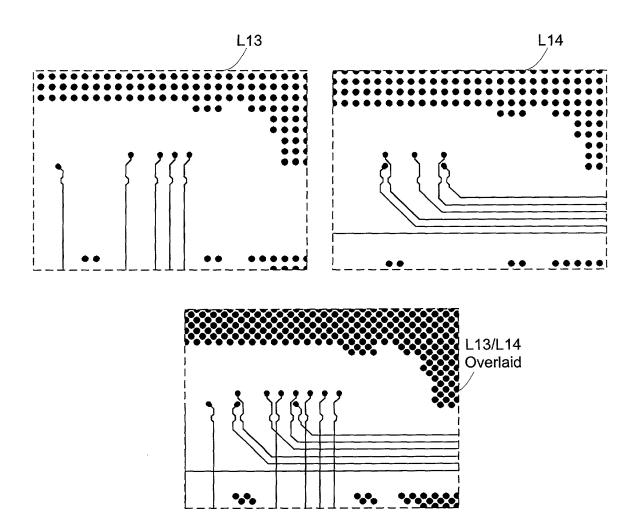


Fig. 25

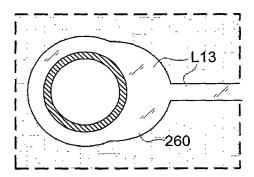


Fig. 26